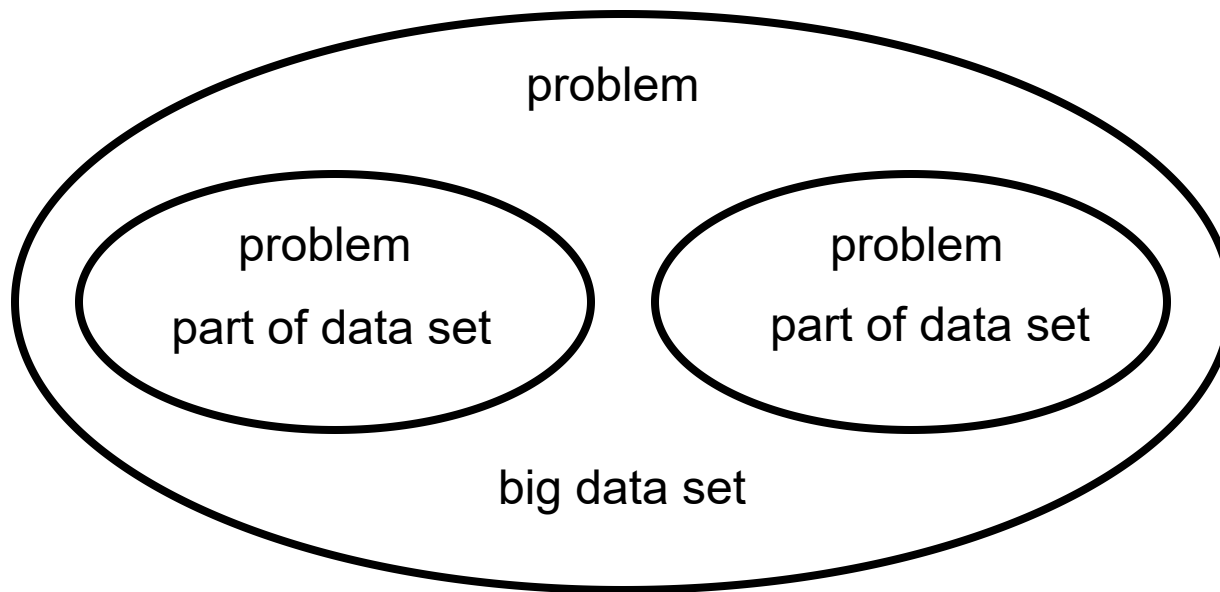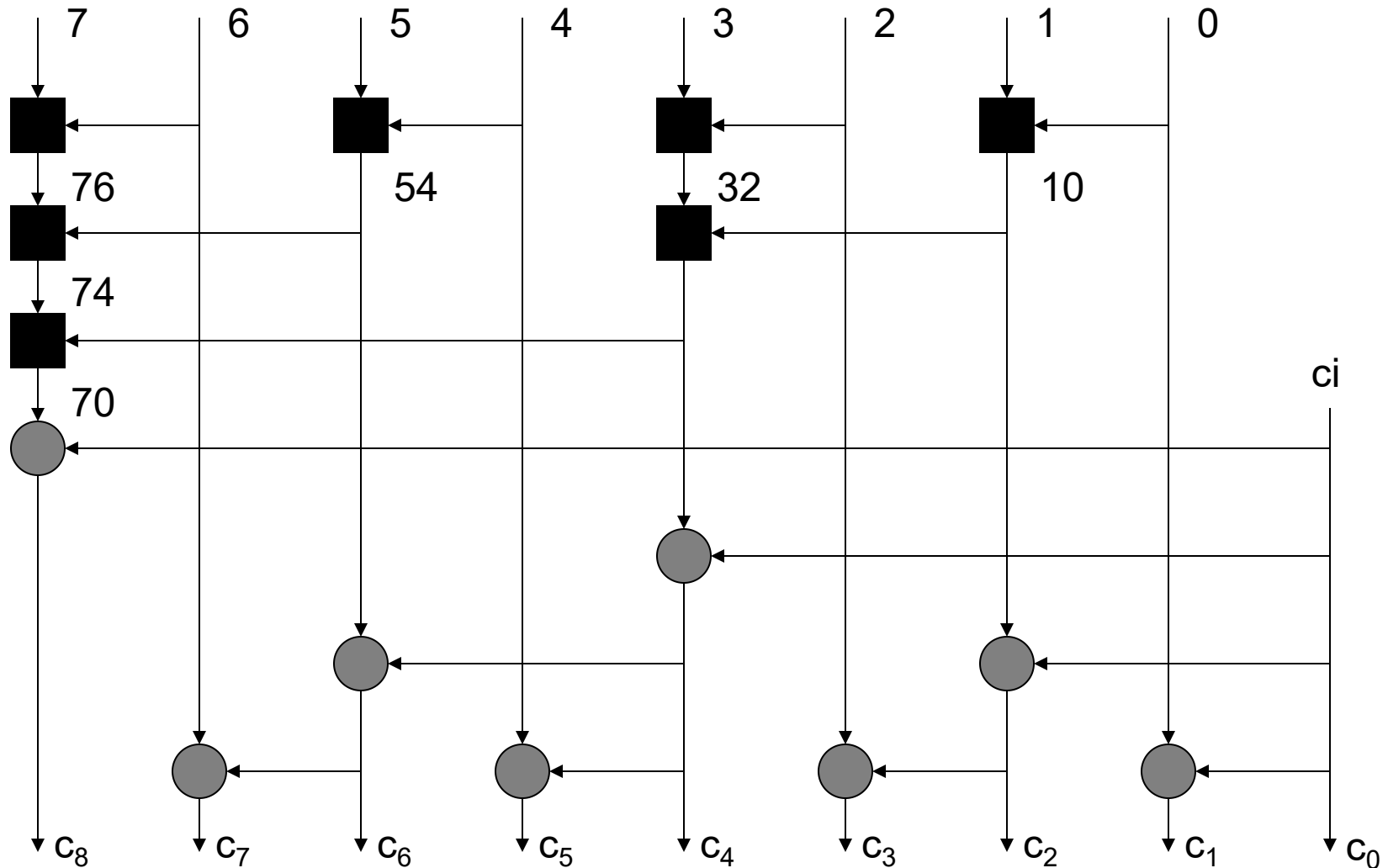# Recursive design
with verilog

Recourse to recursion

- divide et impera
- split the problem set repeatedly, until the set is easy to handle

8 bit Brent-Kung prefix tree

7   6   5   4   3   2   1   0

76   54   32   10

74

70

ci

c_8 c_7 c_6 c_5 c_4 c_3 c_2 c_1 c_0

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8 bit Brent-Kung prefix tree

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